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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/747,993	12/27/2000	Kuo-Long Shih	MR1957-500	3611
7590	07/29/2004			
Rosenberg, Klein & Lee 3458 Ellicott Center Drive-Suite 101 Ellicott City, MD 21043			EXAMINER DUGGINS, ALICIA M	
			ART UNIT 2616	PAPER NUMBER

DATE MAILED: 07/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/747,993

Applicant(s)

SHIH, KUO-LONG

Examiner

Alicia M Duggins

Art Unit

2616

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Nakajima (US6556768).

Regarding claims 1 and 2, Nakajima shows a digital video and audio storing and playing device (41) shown in fig.4, which has:

- A video and audio storing unit shown in fig.1 as a memory card (1) (col.7 ll.39-42) with a verification mechanism by way of the microcomputer (7) forming a judgment as to whether or not digital data is to be stored in the EEPROM unit (9) (col.8 ll.59-64).
- A semiconductor storage medium for storing digital data is shown in fig.1 as the EEPROM (9) (col.8 l.61)
- The semiconductor storage medium (9) is connected to a first control unit shown as a microcomputer (7) in fig. 1 (col.8 ll.18-27) with a verification mechanism (col.8 ll.59-64).

- The semiconductor storage medium, EEPROM (9) and first control unit, microcomputer (7) are packaged as a single product, the memory card (1) as shown in fig.1
- In fig.2, Nakajima shows a semiconductor storage medium (29) and a first control unit (27) connected to an input/output interface via a communication line, which can be a data transfer bus (col.10 ll.19-21). In addition Nakajima teaches that a VTR, which contains the semiconductor storage medium and first control unit, can be connected to an input/output interface (shown as a satellite broadcast receiver) via a bus (col. 23 l.59-col.24 l.3).
- A decompressing and playing unit is shown in fig.2 as a reader/writer (21) (col.10 l.61-col.11 l.3) within the reader/writer is a demodulation circuit (25) that decompresses digital data and restores it to the original data for outputting (col.9 ll.42-45, col.10 ll.12-18)
- The demodulation circuit (25) is connected to a second control unit shown as a microcontroller (27) in fig.2
- The microcontroller (27) has a verification mechanism since a signal of only a predetermined level (the signal level must be verified) is supplied to the demodulation circuit
- The decompressing/playing unit is connected to an a data input/output interface through communication line (32), which can be a data bus

- Fig.9 shows the memory card (1) the first data input/output interface shown as an antenna (22-2) connected to the second data input/output interface which is in the reader/writer (21)
- Nakajima addresses the issue of storing digital data to a storage medium in addition to addressing the practicality of such a device as stated in claim 2 (col.3 ll.54-61)

Regarding claims 3 and 4, as stated above, Nakajima teaches the use of a verification mechanism within the storing unit A video and audio storing unit shown in fig.1 as a memory card (1) (col.7 ll.39-42) with a verification mechanism by way of the microcomputer (7) forming a judgment as to whether or not digital data is to be stored in the EEPROM unit (9) (col.8 ll.59-64).

### **Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alicia M Duggins whose telephone number is (703)305-5621. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on (703) 305-4725. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

TTA  
7/24/04

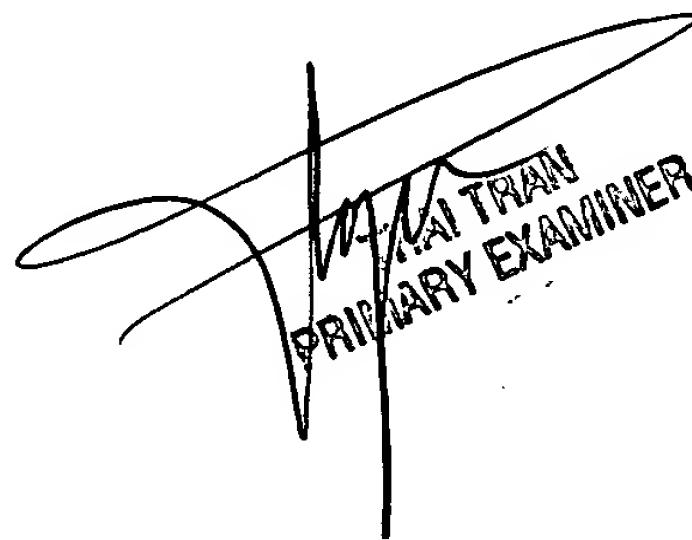
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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AMD  
7/20/04

  
MICHAEL TRAN  
PRIMARY EXAMINER